TRAINING

Bei dem hier beschriebenen Training handelt es sich um ein Cadence Standard Training. Sie erhalten eine Dokumentation in englischer Sprache. Die Trainingssprache ist deutsch, falls nicht anders angekündigt.

Unter http://www.FlowCAD.de/TrainingKontakt.php können Sie sich zum Training anmelden.

Course Title	**Allegro 15.7 to 16.3 Update**
Course Category	**System Interconnect Design – Allegro & OrCAD**
Duration	**2 Days**

Course Description

In this class, you will explore all new functionalities from SPB 15.7 to SPB 16.3.
Design Entry HDL and Allegro® PCB Editor Tool had extensive changes in the SPB16.X release. These main areas are new spacing and physical constraint management, new interactive HDI process, new Graphical User Interface.
The class will use both lectures and hands-on examples using the SPB16.3 software.

Please note that we do offer alternatives for OrCAD Capture CIS customers.

Software Used in This Course

- PS3000: Allegro_PCB_Design L
- PS3100: Performance Option

Course Agenda

What's New in Design Entry HDL (ConceptHDL):

- Operating System Supported
- User Interface Updates
- Displaying Sheet Names in the Hierarchy Viewer
- Enhancements in the Design Publisher Solution ,Publish PDF Utility
- Page Name in tab views and exported to the PDF output
- New Physical and Spacing Classes in Constraint Manager
- New menu to Assigning Signal Models to All the Discrete Components
- New RF-PCB IFF Import
- New Locking CPM Directives
- New Cadence Documentation
- Automatic Table of Contents Generator
- Cross Referencer Links
- Windows Mode
- Docking Properties Window
- Unconnected Pins Indicator
- Physical and Spacing Constraints In the Schematic
- Allegro Design Publisher on Linux
- Font Support
- Usability
- Alignment and Distribution
- Anchor Point Wire Stretch
- Differential Pair Renaming
- Signal Integrity Model Changes

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What's New in Allegro PCB Editor:

- New PCB Editor User Interface "change to OpenGL"
- New Fold-Away Windows
- New Context-sensitive Editing
- New General and EtchEdit Application Mode
- New Color and Visibility Interface
- New Central Location for Design Parameters
- New Transparent Graphics objects and boundaries
- New Quick Access to Commonly Used Subclasses
- New Ratsnest Display by Layer

- New Methodologies concept to manage basics spacing and routing rules
- New Constraint Manager fields "Spacing ,physical ,Region"
- New Assignment mode into Constraint Manager
- Improvements in Constraint Set Management
- Layer Support for Constraint Regions
- XNet Support for Physical and Spacing Constraints

- New RF shape routing and forms Generator
- New RF Flow with Agilent -Import IFF
- New Interactive Component Fanout
- New Super filter mode object command
- New Via Arrays
- Same-Net DRC
- Via-in-Pad DRC
- HDI Via Structures and routing solution
- Dynamic Unused Pad Suppression
- Drill to Metal DRC
- Dynamic Filleting
- Placement Application Mode
- Snap Functions
- Differential Pair Enhancements
- Layer-based Design Partitioning

- Middle Mouse Wheel Functionality
- Long Name Support Productized
- File Manager Command (filemgr)
- Shapes
- Waived DRCs
- Same Net DRC
- New Cadence Help
- Place Replication with routing
- Usability and General Productivity Enhancements
- Resize/Respace of Diff Pair Routes
- Display & Graphics Flip Design , 3D view
- Dynamic Diff Pair Phase Control
- Multi-Line Generator
- Jumpers routing and management

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Audience

This class is designed for PCB Designers and Electrical Engineers who are current users of the Allegro PCB 15.X Editor tool. You must have a good working knowledge of the current use model in order to obtain the most from this class.

Prerequisites

- This class is designed ONLY to update your knowledge base to the current SPB16.3 software. It does not teach you the basic use of the tool.

Special Notes

This class is applicable to all licenses/tiers of the Allegro PCB Editor tool.

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